

Serial No. 09/642,433

February 4, 2004

Reply to the Office Action dated November 18, 2003

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AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

A1
5-3-04
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Claim 1 (currently amended): A data processing device comprising:

- a read-only memory;
- a flash memory capable of modifying information stored therein and adding information thereto;
- a central processing unit performing data processing using information stored in said read-only memory and said flash memory;
- an information storage area provided in said flash memory for storing predetermined modifiable information among the information used by said central processing unit for data processing;
- an address storage area provided in said flash memory for storing at least the address of the information stored in said information storage area; and
- an address-modification control unit for, after at least one of modification of modifiable information stored in said information storage area and addition of modifiable information to said information storage area, and in accordance with said one of the modification of the information and addition of the information, performing one of modification of the address of the information stored in said address storage area and addition of the address of the information to said address storage area; wherein

~~whereby~~ the information used by said central processing unit for data processing can be freely modified or added; and

the address-modification control unit controls a function address table area and a variable address table area.

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Claims 2 and 3 (canceled).

AI

Claim 4 (currently amended): A method in a data processing device comprising:

- a read-only memory;
- a flash memory capable of modifying information stored therein and adding information thereto;
- a central processing unit performing data processing using information stored in said read-only memory and said flash memory;
- an information storage area in said flash memory; and
- an address storage area in said flash memory;

said method comprising the steps of:

- storing in said information storage area predetermined modifiable information among the information used by said central processing unit for data processing;
- storing in said address storage area at least the address of the information stored in said information storage area;
- performing at least one of modification of modifiable information stored in said information storage area and addition of modifiable information to said information storage area; and
- then, in accordance with said one of modification of the information and addition of the information, performing one of modification of the address of the information, stored in said address storage area and addition of the address of the information to said address storage area; wherein

whereby the information used by said central processing unit for data processing can be freely modified or added; and

the address storage area includes a function address table area and a variable address table area.

BI

Claims 5 and 6 (canceled).